EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S73	2236	emulat$3 with integrated with circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 11:10
S74	10611	(simulat$3 emulat $3) with integrated with circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 11:11
S75	163	(simulat$3 emulat $3) with integrated with circuit and schedul $3 with (pin input)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 11:13
S76	42	S75 and ("257" "716").clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 11:14
S77	12724	(simulat$3 emulat $3) with (ICl integrated near circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 11:15
S78	176	(simulat$3 emulat $3) with (ICl integrated near circuit) and schedul$3 with (pin input)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 11:15
S79	10	S78 and "370". clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 11:15
S80	1717	((simulat$3 emulat $3) with (ICl integrated near circuit)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 11:19
S81	421	((emulat$3) with (ICl integrated near circuit)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 11:19
S82	51	S78 and ("257" "716").clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 11:21

S83	1	S78 and ("326"). clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 11:21
S85	75	S78 and ("703"). clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 11:28
S87	52	(simulat$3 emulat $3) with (IC! integrated near circuit) and schedul$3 with (pin input ingress egress output) same (critical frequen$3 order priority precedence)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 11:29
S89	1960	(370/395.4 370/395.41 370/395.42 370/395.43).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 12:09
S90	0	S78 and S89	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 12:10
S91	9	S78 and ("710"). clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/01 12:33
S92	445	(test$3 model$3 software) with (IC! integrated near circuit) and schedul$3 with (pin input ingress egress output) same (critical frequen$3 order priority precedence)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/02 07:59
S94	0	S93 and "326". clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/02 08:02
S95	0	S93 and "257". clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/02 08:02
S96	6	S93 and "716". clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/02 08:02

S97	5	S93 and "703". clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/02 08:02
S98	6	S93 and "710". clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/02 08:03
S99	79	S93 and "370". clas.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2010/12/02 08:03

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